

WEST Search History

DATE: Friday, July 07, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L31	l24 and interface	1
<input type="checkbox"/>	L30	l26 and interface	1
<input type="checkbox"/>	L29	L27 and l1	0
<input type="checkbox"/>	L28	L27 and (parallel same interface)	0
<input type="checkbox"/>	L27	L26 and (communication or transmi\$)	1
<input type="checkbox"/>	L26	L25 and configuration and memory	1
<input type="checkbox"/>	L25	L24 and (parallel and interface) and fpga	1
<input type="checkbox"/>	L24	20020156998	1
<input type="checkbox"/>	L23	L22 not l20	38
<input type="checkbox"/>	L22	L21 and (fpga or programmable)	53
<input type="checkbox"/>	L21	l17 and (configuration same memory)	71
<input type="checkbox"/>	L20	L19 and (fpga or programmable)	23
<input type="checkbox"/>	L19	l18 and configuration	42
<input type="checkbox"/>	L18	L17 and (complian\$ or l1)	44
<input type="checkbox"/>	L17	(parallel adj interface) same (emulat\$ or virtual\$)	208
<input type="checkbox"/>	L16	l12 or l1	10725
<input type="checkbox"/>	L15	l13 and (fpga)	7
<input type="checkbox"/>	L14	l13 and (fpga or programm\$)	103
<input type="checkbox"/>	L13	l11 and L12	104
<input type="checkbox"/>	L12	parallel adj interface	9882
<input type="checkbox"/>	L11	l7 and virtual	122
<input type="checkbox"/>	L10	L1 and fpga	15
<input type="checkbox"/>	L9	L7 and fpga	7
<input type="checkbox"/>	L8	L7 and fpga and virtual\$	7
<input type="checkbox"/>	L7	L1 and emulat\$3	156
<input type="checkbox"/>	L6	L5 not l4	5
<input type="checkbox"/>	L5	L2 and emulat\$3	18
<input type="checkbox"/>	L4	L3 and emulat\$3	13
<input type="checkbox"/>	L3	L2 and virtual\$	116
<input type="checkbox"/>	L2	L1 and ((fpga or programmable) same interface)	176
<input type="checkbox"/>	L1	ieee adj 1284	1162